

## Refine Search

### Search Results -

Term	Documents
(12 AND 3).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	10
(L12 AND L3).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	10

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L32

Refine Search

Recall Text

Clear

Interrupt

### Search History

DATE: Friday, January 25, 2008

[Purge Queries](#)[Printable Copy](#)[Create Case](#)

Set  
Name Query

Hit Set  
Count Name  
 result set

side by  
 side

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

L32 I12 and I3

10 L32

L31 I12 and I17

21 L31

L30 I12 and I16

3 L30

<u>L29</u>	I12 and I15	13	<u>L29</u>
<u>L28</u>	I12 and I14	60	<u>L28</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L27</u>	I12 and I14	6	<u>L27</u>
<u>L26</u>	(interrupt\$4 and flag\$4 and application near4 specific and rewrit\$5 near2 register\$1 and exception\$1).clm.	1	<u>L26</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L25</u>	I9 and I17	24	<u>L25</u>
<u>L24</u>	I9 and I16	0	<u>L24</u>
<u>L23</u>	I9 and I15	7	<u>L23</u>
<u>L22</u>	I9 and I14	11	<u>L22</u>
<u>L21</u>	I7 and I17	32	<u>L21</u>
<u>L20</u>	I7 and I16	13	<u>L20</u>
<u>L19</u>	I7 and I15	22	<u>L19</u>
<u>L18</u>	I7 and I14	81	<u>L18</u>
<u>L17</u>	(710/260-266)![CCLS]	1948	<u>L17</u>
<u>L16</u>	(713/401-601)[CCLS]	4206	<u>L16</u>
<u>L15</u>	(712/36,227,244,248)[CCLS]	1766	<u>L15</u>
<u>L14</u>	(712/2-300)[CCLS]	14119	<u>L14</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L13</u>	L11 and I9	37	<u>L13</u>
<u>L12</u>	L11 and I7	195	<u>L12</u>
<u>L11</u>	(inhibit\$4 or hold\$3 or halt\$3 or held or suspend\$3 or suspension\$1) near8 (interrupt\$1 or exception\$1)	21838	<u>L11</u>
<u>L10</u>	L9 and I3	2	<u>L10</u>
<u>L9</u>	I6 and (application near3 specific or asic)	110	<u>L9</u>
<u>L8</u>	L7 and I3	21	<u>L8</u>
<u>L7</u>	L6 and I4	397	<u>L7</u>
<u>L6</u>	L5 and (nested or multip\$7 or plur\$7 or dual or second) near10 (exception\$1 or interrupt\$4)	1688	<u>L6</u>
<u>L5</u>	(exception\$1 or Interrupt\$5) near15 (indicat\$4 or notic\$3 or notif\$7 or detect\$4) near45 return\$5	4365	<u>L5</u>

<u>L4</u>	(delay\$1 or wait\$1 ) near8 (cycle\$1 or clock\$1 or period\$1)	190439	<u>L4</u>
<u>L3</u>	(rewrit\$4 or rewritable) near5 register	3725	<u>L3</u>
<u>L2</u>	L1 and (nested or multip\$7 or plur\$7 or dual or second) near10 interrupt\$4	1406	<u>L2</u>
<u>L1</u>	Interrupt\$5 near15 (indicat\$4 or notic\$3 or notif\$7 or detect\$4) near45 return\$5	3592	<u>L1</u>

END OF SEARCH HISTORY



Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((((interrupt or exception) and application and specific)&lt;in&gt;metadata)&lt;and&gt;((instruct..."

Your search matched 16 of 123 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.



## » Search Options

[View Session History](#)[New Search](#)

## » Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

## Modify Search

 
☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract[IEEE/IET](#)[Books](#)[Educational Courses](#)

IEEE/IET journals, transactions, letters, magazines, conference proceedings, and standards.

[Select All](#) [Deselect All](#)

- ☐ 1. **MCGREP--A Predictable Architecture for Embedded Real-Time Systems**  
Whitham, J.; Audsley, N.;  
[Real-Time Systems Symposium, 2006. RTSS '06. 27th IEEE International](#)  
Dec. 2006 Page(s):13 - 24  
Digital Object Identifier 10.1109/RTSS.2006.28  
[AbstractPlus](#) | Full Text: [PDF\(179 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 2. **Data-stationary controller for 32-bit application-specific RISC**  
Lee, M.K.; Choi, B.Y.; Lee, K.Y.; Lee, S.H.;  
[Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on](#)  
3-6 May 1993 Page(s):1933 - 1936 vol.3  
Digital Object Identifier 10.1109/ISCAS.1993.394128  
[AbstractPlus](#) | Full Text: [PDF\(344 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 3. **Fast dynamic analysis of complex HW/SW-systems based on abstract state machine mode**  
Del Castillo, G.; Hardt, W.;  
[Hardware/Software Codesign, 1998., \(CODES/CASHE '98\). Proceedings of the Sixth International](#)  
15-18 March 1998 Page(s):77 - 81  
Digital Object Identifier 10.1109/HSC.1998.666241  
[AbstractPlus](#) | Full Text: [PDF\(120 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 4. **Application of intelligent multimedia system in an automation-intensive production line**  
Krishnan, S.M.; Swaminathan, S.;  
[Knowledge-Based Intelligent Electronic Systems, 1997., KES '97, Proceedings., 1997 First Internat](#)  
Volume 1, 21-23 May 1997 Page(s):248 - 254 vol.1  
Digital Object Identifier 10.1109/KES.1997.616916  
[AbstractPlus](#) | Full Text: [PDF\(424 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 5. **Exploring the diversity of multimedia systems**  
Johnson Kin; Chunho Lee; Mangione-Smith, W.H.; Potkonjak, M.;

[Very Large Scale Integration \(VLSI\) Systems, IEEE Transactions on](#)  
Volume 9, Issue 3, June 2001 Page(s):474 - 485  
Digital Object Identifier 10.1109/92.929581  
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(408 KB\)](#) IEEE JNL  
[Rights and Permissions](#)

- ☐ 6. **New custom computing machine dedicated to fast dynamic configuration applications**  
Rabel, C.E.; Sawan, M.;  
[Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IEEE International C](#)  
Volume 2, 5-8 Sept. 1999 Page(s):957 - 960 vol.2  
Digital Object Identifier 10.1109/ICECS.1999.813391  
[AbstractPlus](#) | [Full Text: PDF\(316 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 7. **Processor support for temporal predictability - the SPEAR design example**  
Delvai, M.; Huber, W.; Puschner, P.; Steininger, A.;  
[Real-Time Systems, 2003. Proceedings, 15th Euromicro Conference on](#)  
2-4 July 2003 Page(s):169 - 176  
[AbstractPlus](#) | [Full Text: PDF\(261 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 8. **System support for embedded applications**  
Ramamritham, K.; Arya, K.;  
[VLSI Design, 2003. Proceedings, 16th International Conference on](#)  
4-8 Jan. 2003 Page(s):22  
Digital Object Identifier 10.1109/ICVD.2003.1183109  
[AbstractPlus](#) | [Full Text: PDF\(195 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 9. **Automatic generation of fast timed simulation models for operating systems in SoC design**  
Yoo, S.; Nicolescu, G.; Gauthier, L.; Jerraya, A.A.;  
[Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings](#)  
4-8 March 2002 Page(s):620 - 627  
Digital Object Identifier 10.1109/DATE.2002.998365  
[AbstractPlus](#) | [Full Text: PDF\(341 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 10. **PEAS-III: an ASIP design environment**  
Itoh, M.; Higaki, S.; Sato, J.; Shiomi, A.; Takeuchi, Y.; Kitajima, A.; Imai, M.;  
[Computer Design, 2000. Proceedings, 2000 International Conference on](#)  
17-20 Sept. 2000 Page(s):430 - 436  
Digital Object Identifier 10.1109/ICCD.2000.878319  
[AbstractPlus](#) | [Full Text: PDF\(676 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 11. **An efficient hardware/software co-design implementation for broadband telecommunication**  
Rower, T.; Stadler, M.; Felber, N.; Fichtner, W.; Thalmann, M.;  
[Global Telecommunications Conference, 1998. GLOBECOM 98. The Bridge to Global Integration](#)  
Volume 3, 8-12 Nov. 1998 Page(s):1676 - 1681 vol.3  
Digital Object Identifier 10.1109/GLOCOM.1998.776673  
[AbstractPlus](#) | [Full Text: PDF\(432 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 12. **An embedded stack microprocessor for SDH telecommunication applications**  
Stadler, M.; Thalmann, M.; Rower, T.; Felber, N.; Fichtner, W.;  
[Custom Integrated Circuits Conference, 1998... Proceedings of the IEEE 1998](#)  
11-14 May 1998 Page(s):17 - 20  
Digital Object Identifier 10.1109/CICC.1998.694899  
[AbstractPlus](#) | [Full Text: PDF\(484 KB\)](#) IEEE CNF  
[Rights and Permissions](#)

- ☐ 13. **Casablanca II: implementation of a real-time RISC core for embedded systems**  
Tanaka, K.;  
[Application-Specific Systems, Architecture Processors, 2005. ASAP 2005. 16th IEEE International](#)  
23-25 July 2005 Page(s):36 - 42  
Digital Object Identifier 10.1109/ASAP.2005.22  
[AbstractPlus](#) | Full Text: [PDF\(224 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 14. **Proceedings. 2003 International Symposium on System-on-Chip (IEEE Cat. No.03EX748)**  
[System-on-Chip, 2003. Proceedings, International Symposium on](#)  
19-21 Nov. 2003  
Digital Object Identifier 10.1109/ISSOC.2003.1267700  
[AbstractPlus](#) | Full Text: [PDF\(404 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 15. **An ASIC RISC-based I/O processor for computer applications**  
Cates, R.L.; Farrell, J.J., III;  
[Euro ASIC '90](#)  
29 May-1 June 1990 Page(s):50 - 55  
Digital Object Identifier 10.1109/EASIC.1990.207909  
[AbstractPlus](#) | Full Text: [PDF\(484 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 16. **A microprocessor in 4 months. Development of the FHOP-microprocessor-kernel via VHDL**  
Jansen, D.; Gieringer, T.; Zimpfer, F.;  
[ASIC Conference and Exhibit, 1994. Proceedings., Seventh Annual IEEE International](#)  
19-23 Sept. 1994 Page(s):79 - 85  
Digital Object Identifier 10.1109/ASIC.1994.404604  
[AbstractPlus](#) | Full Text: [PDF\(500 KB\)](#) IEEE CNF  
[Rights and Permissions](#)

[Help](#) [Contact](#)[Copy](#)Indexed by  
 Inspec®